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Handbook of Silicon Wafer Cleaning Technology, Third Edition, provides an in-depth discussion of cleaning, etching and surface conditioning for semiconductor applications. The fundamental physics and chemistry associated with wet and plasma processing are reviewed, including surface and colloidal aspects.

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HANDBOOK OF SEMICONDUCTOR WAFER CLEANING TECHNOLOGY

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Prior to forming a consulting company, Karen was employed at Novellus Systems, AMD, and Cypress Semiconductor. She has published more than 30 technical publications, has been awarded seven patents, and is co-editor of Handbook of Silicon Wafer Cleaning Technology.

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Cleaning Procedures for Silicon Wafers INRF application note
Process name: SOLVENTCLEAN + RCA01 + HFDIP . W. Kern and J. Vossen, Thin Film Processes, Academic Press: New York, 1978, Ch V-1 . W. Kern and Ed., Handbook of Semiconductor Cleaning Technology, Noyes Publishing: Park Ridge, NJ, 1993 Ch 1.
Checklist

Cleaning Procedures for Silicon Wafers

The second Edition of the Handbook of Silicon Wafer Cleaning Technology is intended to provide knowledge of wet, plasma, and other surface conditioning techniques used to manufacture integrated circuits. The integration of the clean processes into the device manufacturing flow will be presented with respect to other manufacturing steps such as thermal, implant, etching, and photolithography ...

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